



501.39484X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): S. YAMADA et al
Serial No.: 09/767,830
Filed: January 24, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND PROCESS FOR MANUFACTURING THE SAME
Group: 2812
Examiner: J. KENNEDY
Conf. No. 2196

AMENDMENT

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

February 9, 2004

Sir:

In response to the Office Action November 7, 2003, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Specification.

Amendments to the Claims.

Remarks are included following the Amendment.

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